

101a,101b	PHOTODIODE ARRAY	102a,102b	READOUT GATE
103a,103b	CCD SHIFT REGISTER	104a,104b	OUTPUT CIRCUIT
105a,105b	SHUTTER GATE	106	SHUTTER DRAIN

FIG. 1 (BACKGROUND ART)



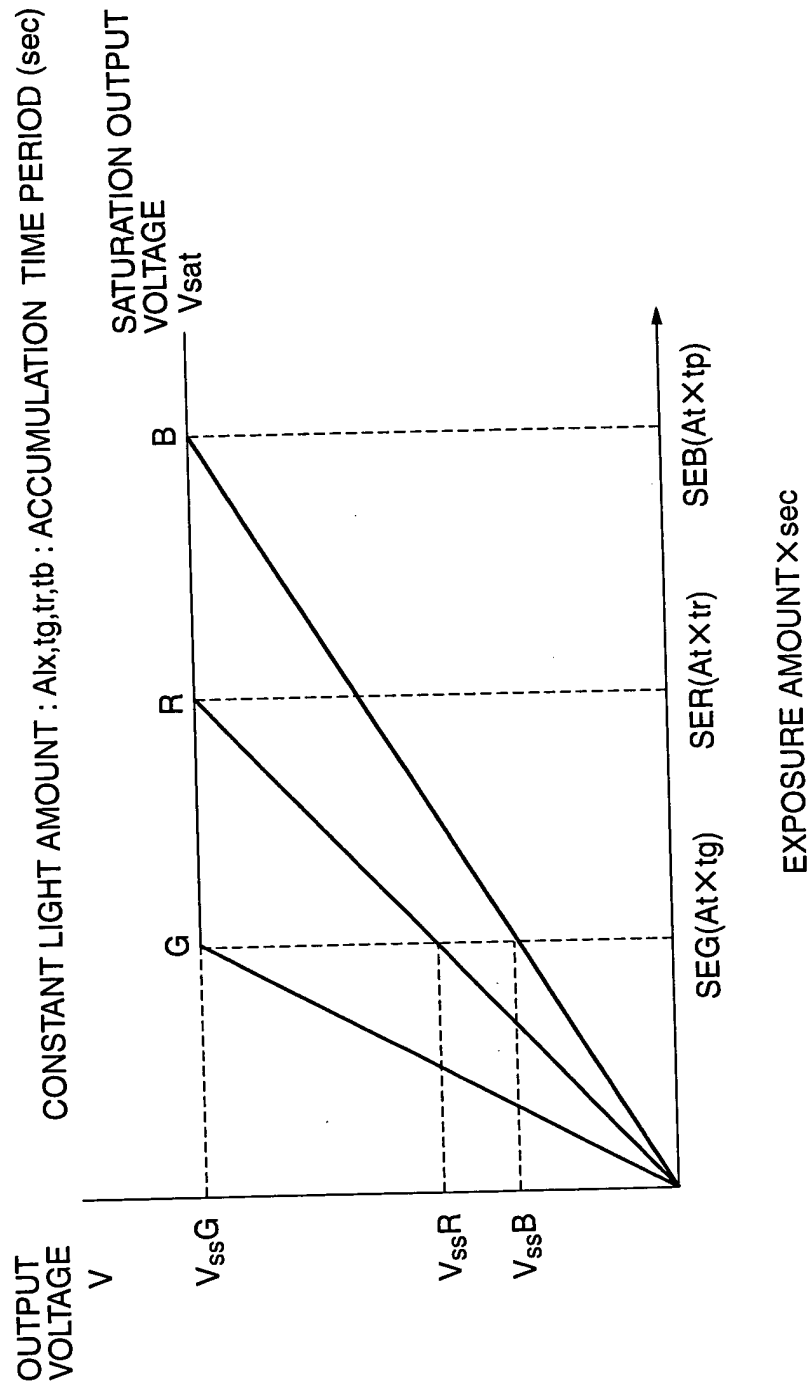


FIG. 3 (BACKGROUND ART)

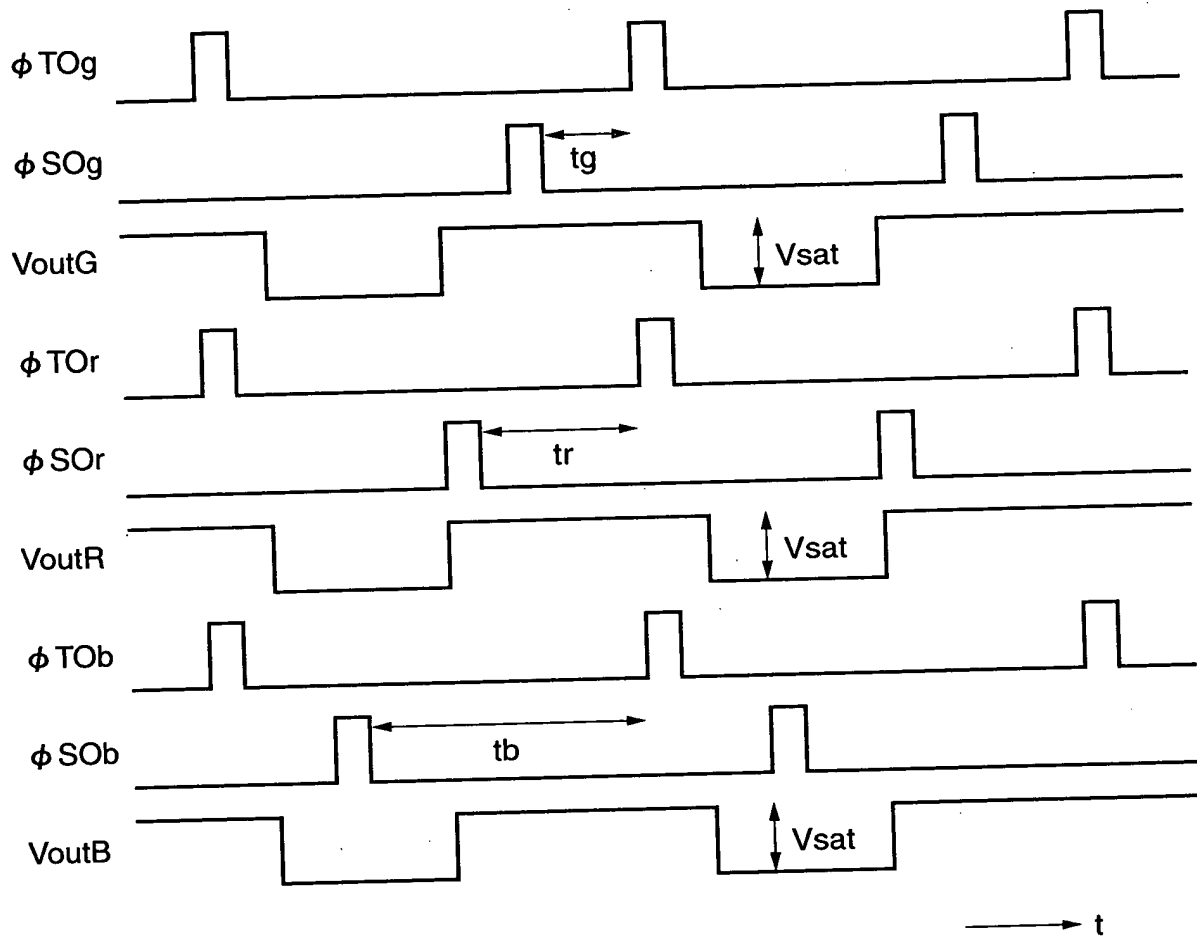
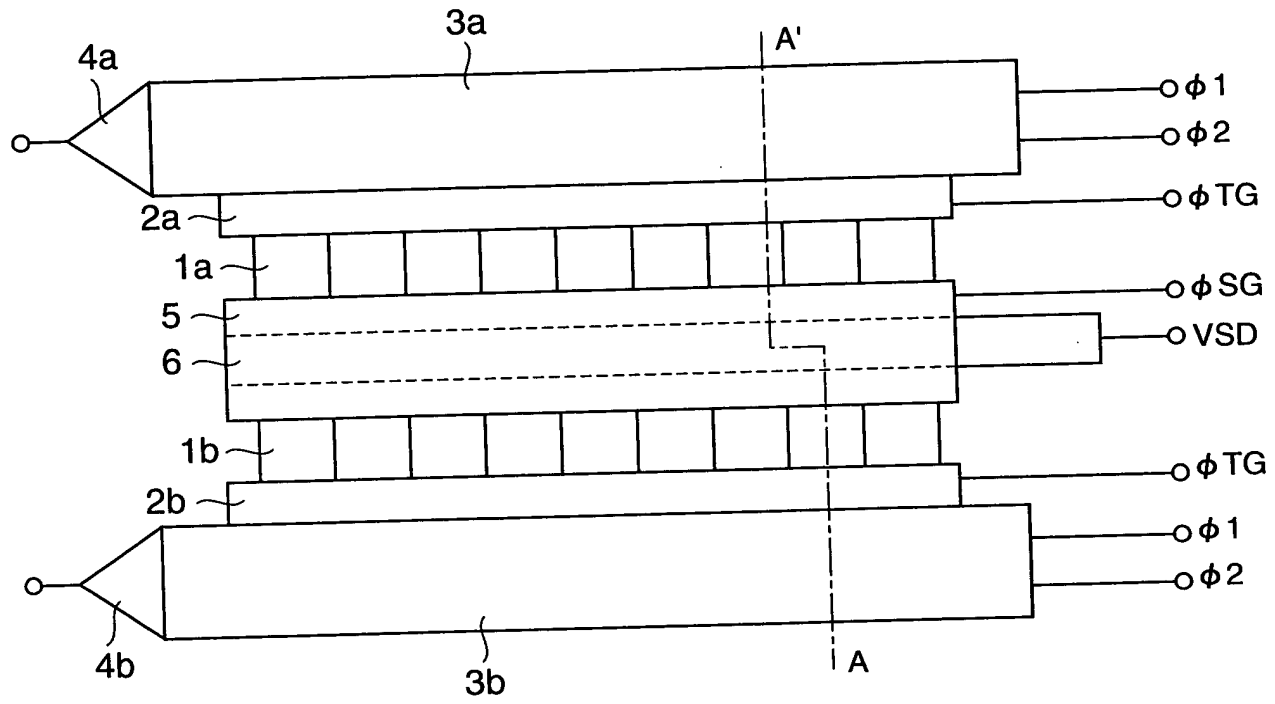


FIG. 4 (BACKGROUND ART)



1a,1b PHOTODIODE ARRAY  
3a,3b CCD SHIFT REGISTER  
5 SHUTTER GATE

2a,2b READOUT GATE  
4a,4b OUTPUT CIRCUIT  
6 SHUTTER DRAIN

FIG. 5

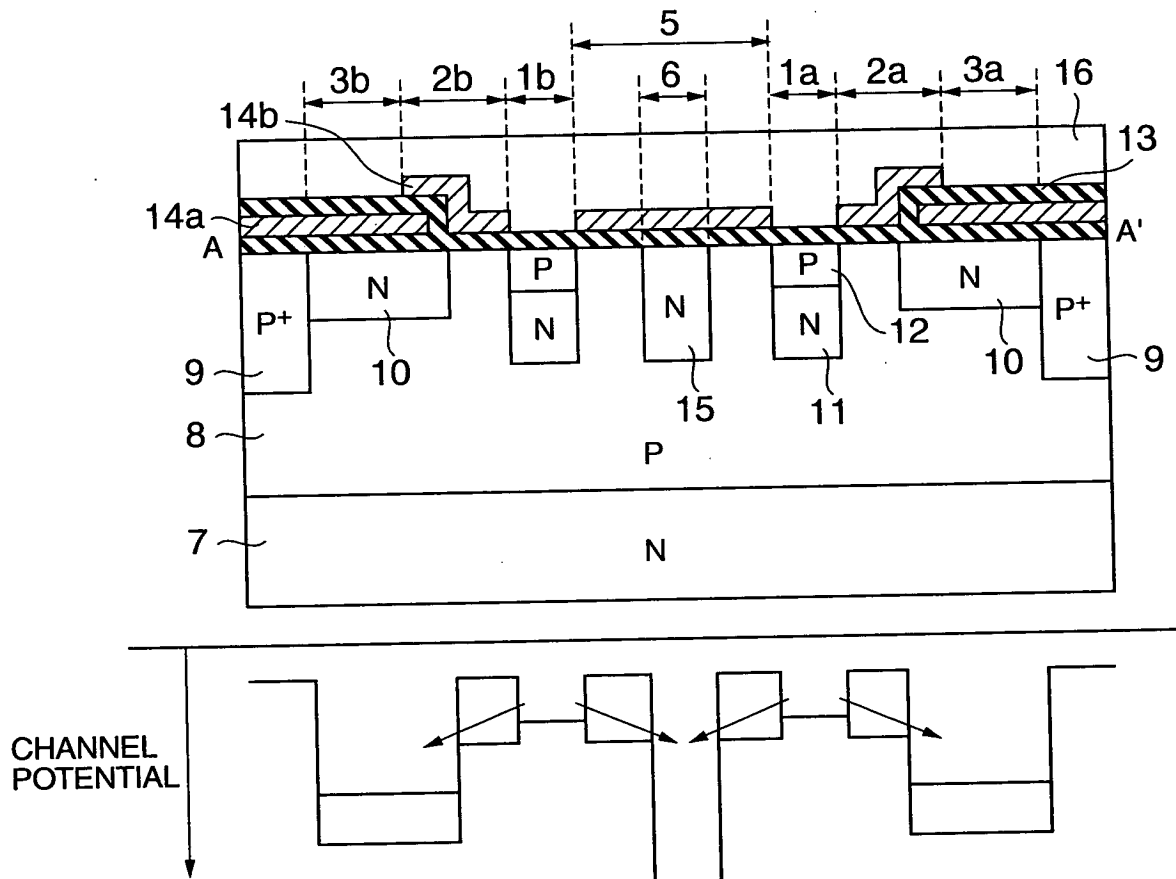
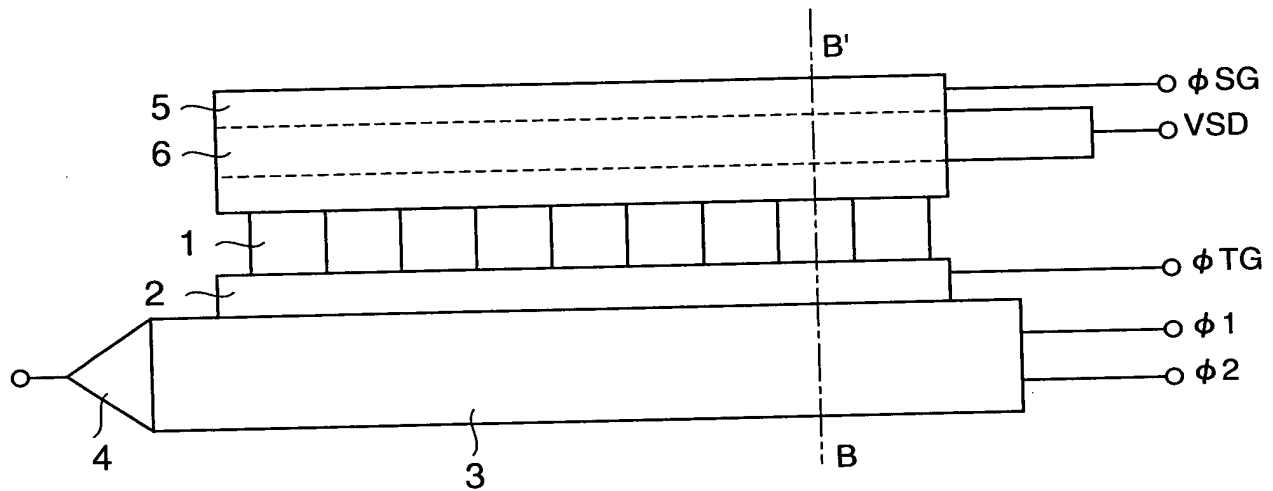


FIG. 6

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- |   |                    |   |                |
|---|--------------------|---|----------------|
| 1 | PHOTODIODE ARRAY   | 2 | READOUT GATE   |
| 3 | CCD SHIFT REGISTER | 4 | OUTPUT CIRCUIT |
| 5 | SHUTTER GATE       | 6 | SHUTTER DRAIN  |

FIG. 7

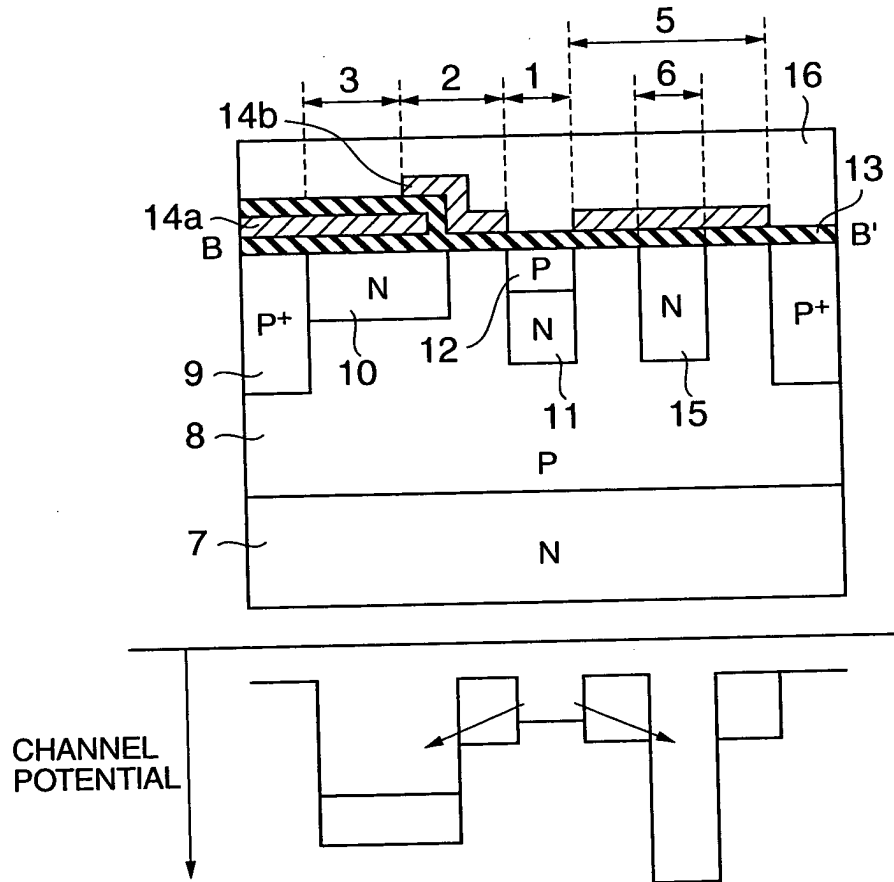


FIG. 8